

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3997	365/230.06.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/30 12:29
L2	2779	365/149.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/30 12:29
L3	528	source near5 drain near5 gate near5 connected adj5 together	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/30 12:30
L4	16	chen-chieng-chung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/30 12:30

Day : Thursday
Date: 11/30/2006

Time: 12:26:14

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = CHEN

First Name = CHIENG-CHUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10378231	6839294	150	03/03/2003	MEMORY DEVICE WITH HIGH CHARGING VOLTAGE BIT LINE	CHEN, CHIENG-CHUNG
10604821	6930947	150	08/20/2003	POWER SOURCE DETECTOR	CHEN, CHIENG-CHUNG
10605080	7075359	150	09/08/2003	TWO PHASE INTERNAL VOLTAGE GENERATOR	CHEN, CHIENG-CHUNG
10696525	Not Issued	71	10/29/2003	Memory pumping circuit	CHEN, CHIENG-CHUNG
10761216	Not Issued	61	01/22/2004	Temperature sensing variable frequency generator	CHEN, CHIENG-CHUNG

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name
	<input type="text" value="chen"/>	<input type="text" value="chieng-chung"/>
		<input type="button" value="Search"/>

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